



SILICON LABS

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Silicon Labs Introduces Industry's First Any-Rate, Any-Output Clock Generator

Si5338 Simplifies Timing Architectures by Replacing Four Discrete PLLs

AUSTIN, Texas--(BUSINESS WIRE)--Nov. 3, 2008--Silicon Laboratories Inc. (NASDAQ: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today introduced a new family of clock generators and buffers that provide the most frequency flexibility in the industry. Based on Silicon Labs' breakthrough MultiSynth technology, the Si5338 is capable of synthesizing any frequency from 0.16 to 350 MHz and select frequencies to 700 MHz on each of the device's four differential outputs, dramatically simplifying timing architectures by replacing four discrete phase-locked loop (PLL) devices with a single IC. Consistent with Silicon Labs' timing strategy of providing the most frequency flexible clock and oscillator solutions in the industry, the Si5338 provides best-in-class performance and integration while shortening design cycles for applications such as next generation communications equipment, wireless base stations, broadcast video, test and measurement and data acquisition.

At one picosecond rms random jitter typical, the Si5338 is able to simultaneously generate low jitter clocks for a wide variety of ICs, including processors, FPGAs, ASICs, memory and physical layer transceivers. The device generates four differential or eight single-ended outputs per device, eliminating the need for external clock distribution buffers. In addition to frequency, each output clock is independently configurable in terms of supply voltage (1.5V, 1.8V, 2.5V, 3.3V) and signal format (LVPECL, LVDS, CMOS, HCSL, SSTL, HSTL), eliminating the need for external level translators and thereby reducing BOM cost and complexity.

Historically, complex timing architectures have required multiple clock generators and/or standalone crystal oscillators (XOs) to provide the range of frequencies needed by the end application, often at the expense of cost, design complexity and power. The any-rate, any-output capability provided by the Si5338 dramatically simplifies timing architectures by replacing fixed frequency clock generators, discrete level translators and crystal oscillators with a single device, minimizing cost and real estate and reducing power by 50 percent compared to traditional solutions. To simplify board-level test, the Si5338 includes a frequency margining feature that enables the frequency of each output clock to be varied dynamically over the 0.16 to 350 MHz range, eliminating discrete XOs and making it easier for hardware designers to guarantee consistent, reliable system operation over temperature and voltage.

For applications that do not require the programmability provided by the Si5338, Silicon Labs is also introducing a broad portfolio of Si5334 pin-controlled clock generators. More than 70 Si5334 devices are available for popular Ethernet, Fibre Channel, PCI Express, T1/E1, broadcast video (HDTV) and SONET/SDH OC-3/12/48 applications. In addition, Silicon Labs is introducing the Si5330 family of low-jitter, low-skew clock buffers that support the widest range of output clock formats in the industry. The new Si5330 flexible clock buffer produces 4 or 8 buffered copies of the input clock and is available in both differential and single-ended versions.

"2008 has been a breakthrough year for Silicon Labs timing products. We have added more than 100 devices to our overall portfolio" said Dave Bresemann, vice president of Silicon Labs. "The new product family of any-rate, any-output clock generators and clock buffers complements Silicon Labs' existing products, providing the industry's broadest portfolio of timing products and enabling Silicon Labs to provide customers a complete clock-tree solution for next-generation designs."

In addition to the new family of any-rate, any-output clock generators and clock buffers, Silicon Labs offers a broad portfolio of frequency flexible, low jitter any-rate, jitter attenuating clock multipliers and low jitter silicon oscillators, XOs and VCXOs. These products use optimized DSPLL[®] technology to shorten lead times and deliver outstanding jitter performance for datacom, telecom, wireless, broadcast video, test and measurement, and consumer markets.

Pricing and Availability

Packaged in a space-saving 24-lead 4x4mm QFN package, the Si5338, Si5334 and Si5330 are available now. Si5338 pricing starts at \$9.31 in 10,000 piece quantities and is dependent on output frequency.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of highly integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These

patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The company is committed to contributing to our customers' success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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CONTACT: Silicon Laboratories Inc.
Lindsey Starnes, +1 512-532-5349
lindsey.starnes@silabs.com

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