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Silicon Labs and Xilinx Partner to Deliver Industry-Leading Jitter Performance and Flexibility for Broadcast Video

Broadcast Connectivity Kit Leverages Lowest Jitter Clock Multiplier to Maximize Link Integrity

AUSTIN, Texas, Sep 13, 2010 (BUSINESS WIRE) -- Silicon Laboratories Inc. (NASDAQ: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced support for a range of Xilinx development platforms that increase flexibility and performance in applications such as digital broadcast video. Xilinx, a leader in complete programmable logic solutions, leverages Silicon Labs' frequency-flexible timing IC solutions in a number of its latest development platforms, including the new Spartan-6 FPGA Broadcast Connectivity Kit, where Silicon Labs' low jitter technology maximizes link integrity.

Xilinx's Virtex-6 FPGA and Spartan-6 FPGA Broadcast Connectivity Kits use Silicon Labs' Si5324 clock IC to address the increasing complexity of clock synthesis in broadcast video applications. These applications require a frequency flexible solution for clock synchronization and generation in high-definition serial digital interface (HD-SDI) and 3G SDI designs. The Si5324 generates a wide range of video clock rates synchronized to an HYSNC signal or reference clock input while eliminating the need for external VCXO and loop filter components. In addition, the Si5324 delivers programmable sub-10 Hz jitter attenuation bandwidth and industry-leading jitter performance of 5 picoseconds peak to peak, providing significant margin to 3G SDI standards. By meeting these standards with considerable margin, the jitter budget that would otherwise be allocated to clock generation can be applied to other system components, simplifying component selection and design.

"Xilinx focuses on enabling customers to get to market faster by providing an FPGA development platform that gives them the flexibility and performance needed to reduce design cycles," said Raj Seelam, senior marketing manager, Platform Solutions at Xilinx. "The programmability of Silicon Labs' timing solutions offers an innovative way to evaluate different frequency plans without changing hardware or increasing the bill of materials, enabling a single IC design to address multi-frequency applications."

Silicon Labs' complementary crystal oscillators (XOs) and voltage-controlled crystal oscillators (VCXOs) are also ideal for broadcast video applications, providing low jitter clock generation and support for any frequency from 10 to 1.4 GHz, any format (CMOS, LVDS, LVPECL, CML) and any supply voltage (1.8, 2.5, or 3.3 V) with guaranteed jitter as low as 0.5 ps rms (max). The Si53x/Si55x/Si59x/Si57x family includes single, dual, quad and any-frequency I2C programmable XO/VCXOs capable of supporting any video or audio frequency using a single XO/VCXO. These devices can dramatically simplify clock generation and reduce BOM cost in multi-rate HD and 3G SDI applications.

Pricing and Availability

For more information about the Spartan-6 FPGA or Virtex-6 FPGA Broadcast Connectivity Kit or to purchase these kits, please contact Xilinx or visit www.xilinx.com/broadcast.

For more information about Silicon Labs timing reference designs, please visit www.silabs.com/pr/timingreferencedesigns.

For more information or to purchase Silicon Labs' timing ICs, including programmable XO/VCXOs, CMOS-based silicon oscillators, high-performance clock generators, low-jitter clock multipliers, buffers and physical layer timing devices, visit www.silabs.com/pr/clocksoscillators.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of highly integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The company is committed to contributing to our customers' success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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