

Silicon Laboratories Introduces Single Input/Output Any-Rate Clock Multiplier

-- Si5319 is Ideal Low Jitter Clock Source in High Performance Applications --

AUSTIN, Texas--(BUSINESS WIRE)--March 24, 2008--Silicon Laboratories Inc. (Nasdaq: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced the expansion of its broad portfolio of reconfigurable, frequency-agile precision clocks to include a single input, single output jitter-attenuating clock multiplier IC. The new Si5319 Any-Rate Precision Clock is capable of generating any output frequency from either a crystal or reference clock input with 0.3 picoseconds jitter generation. The device supports a free-run mode of operation, enabling the device to be used as a frequency flexible, low jitter clock generator when supplied a crystal input. The Si5319 is ideal for providing clock synthesis, clock multiplication and jitter attenuation in high performance timing applications such as SONET/SDH/OTN line cards, WDM line cards, wireless basestations, synchronous Ethernet routers, test and measurement equipment and broadcast video.

The Si5319 is based on Silicon Labs' patented, third generation DSPLL[®] technology, which provides any-rate frequency synthesis and jitter attenuation in a highly integrated phase-locked loop (PLL) solution that eliminates the need for external voltage-controlled crystal oscillator (VCXO) and loop filter components. The Si5319 provides superior frequency flexibility compared to competing IC and quartz-crystal based devices given its ability to accept any frequency from 2 kHz to 710 MHz and generate any frequency from 2 kHz to 945 MHz and select frequencies to 1.4 GHz.

The Si5319 provides a highly integrated, cost-effective jitter attenuation solution for next-generation multi-rate line cards that must support a broad array of client-side and line-side interfaces, including SONET/SDH, 1G/10G Ethernet, Fiber Channel, OTN and HD-SDI. The timing architecture is dramatically simplified because a single Si5319 can generate all required reference frequencies with extremely low jitter, eliminating the need for multiple high frequency VCXOs.

The Si5319 free-run mode of operation greatly simplifies clock startup issues in high performance applications. In these systems, customers typically use a high frequency crystal oscillator (XO) to generate an initial reference clock for the transceiver driving the high speed fiber optic link. After initial startup, the system requires a reference clock that is synchronized to another clock in the system not available at startup. Traditional approaches require external multiplexer (mux) circuitry to switch between the high frequency XO and the long-term reference clock. The Si5319 solves this problem by locking to an inexpensive crystal input at startup and switching to an active input clock when available. No external mux components are required and the clock switchover is seamless, minimizing output clock phase transients that would otherwise generate bit-errors in the high speed fiber optic interface.

"With the addition of the Si5319, Silicon Labs continues to expand the industry's broadest portfolio of high performance clock and oscillator products," said Dave Bresemann, vice president of Silicon Labs. "Silicon Labs' ability to leverage its mixed-signal design expertise to the timing market has revolutionized the way timing systems are designed, ultimately improving performance and reliability and reducing system complexity."

Silicon Labs' offers a broad portfolio of highly integrated, high performance timing solutions for datacom, telecom, wireless, video, and test and measurement markets. In addition to the Any-Rate precision clock family, Silicon Labs' Si5xx family of oscillators (XOs) and voltage-controlled oscillators (VCXOs) offer the industry's shortest lead times, highest reliability and unmatched performance. These products use optimized DSPLL[®] technology to shorten cycle times while eliminating the need for complex resonator fabrication and tuning. To simplify product selection, Silicon Labs offers a web-based utility (www.silabs.com/VCXOPartNumber) that validates and generates part numbers for single, dual, quad and any-rate frequency devices.

Pricing and Availability

The Si5319 is available now in a 6 x 6 mm, 36-lead QFN package. The device is available in three speed grades based on maximum output clock frequency. Pricing for the lowest speed grade device, which supports a clock output frequency range of 2 kHz to 346 MHz, is \$19.32 in quantities of 1K.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of highly integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These

patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The company is committed to contributing to our customers' success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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