Silicon Labs Introduces Online Clock Tree Design Service

Complements Very Short Clock and Oscillator Lead Time to Speed Time to Market

AUSTIN, Texas, Jul 14, 2010 (BUSINESS WIRE) -- Silicon Laboratories Inc. (NASDAQ: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced an online Clock Tree Design Service that gives customers quick access to Silicon Labs' experienced applications engineering team to provide custom timing architecture proposals that simplify design, reduce BOM costs and minimize risk during development. When combined with the industry's shortest component lead times of two weeks or less, Silicon Labs' clock and oscillator family is designed to accelerate customers' time to market.

Performance-sensitive applications often require a combination of oscillators, clock generators and clock buffers to provide critical reference timing to high-speed SerDes devices, FPGAs, processors, data converters (ADC/DACs) and digital signal processors (DSPs). Not only is individual timing component selection critical, but system-level requirements also need to be taken into account to optimize performance.

Silicon Labs' online Clock Tree Design Service enables customers to enter their system-level timing requirements using a web-based utility, specifying multiple parameters including the number of clock inputs and outputs, input and output frequencies, signal formats and clock jitter. Silicon Labs' applications engineering team reviews the requirements and provides a timing architecture optimized for performance, cost and lead time. Timing proposals are provided in just three business days, providing rapid feedback to customers.

"Silicon Labs' applications team has extensive experience with high-speed PCB design, signal integrity, signal termination, power supply noise rejection and other challenges common to high-performance applications," said Mike Petrowski, general manager of timing products for Silicon Laboratories. "Our online Clock Tree Design Service enables customers to solicit rapid feedback, accelerating the product development process while reducing risk and ensuring next-generation hardware designs are optimized for system performance."

Accelerating Design for Equipment Makers

Silicon Labs' extensive timing portfolio and online customer support capabilities dramatically accelerate time to market and streamline the development process. In addition to the Clock Tree Design Service, Silicon Labs also offers the ClockBuilder (TM) web utility to enable customers to quickly develop custom, application-specific clock generators that support any combination of user-specified input/output frequencies. The company's custom oscillator utility enables developers to specify a custom oscillator, build a part number and order samples in minutes.

Silicon Labs' complete family of clocks and XO/VCXOs is available with the industry's shortest lead times. Available with lead times of two weeks or less, Silicon Labs' custom timing devices do not require non-recurring engineering (NRE) charges.

Silicon Labs Timing Products

Silicon Labs offers the industry's broadest portfolio of clocks and oscillators for networking, communications, embedded computing, broadcast video and consumer applications. This comprehensive portfolio of high-performance jitter attenuating clock multipliers, clock generators, clock buffers, crystal oscillators (XO) and voltage-controlled crystal oscillators (VCXO) leverages Silicon Labs' patented DSPLL[(R)], MultiSynth and silicon oscillator technologies to eliminate expensive discrete components while improving performance, minimizing board space and simplifying system design. For more information, please visit www.silabs.com/timing.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of highly integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The

company is committed to contributing to our customers' success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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